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Finance, Treasury & Investor Relations



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■ Infineon at a Glance

■ Power Semiconductors and Manufacturing

■ Results and Outlook

Disclaimer:

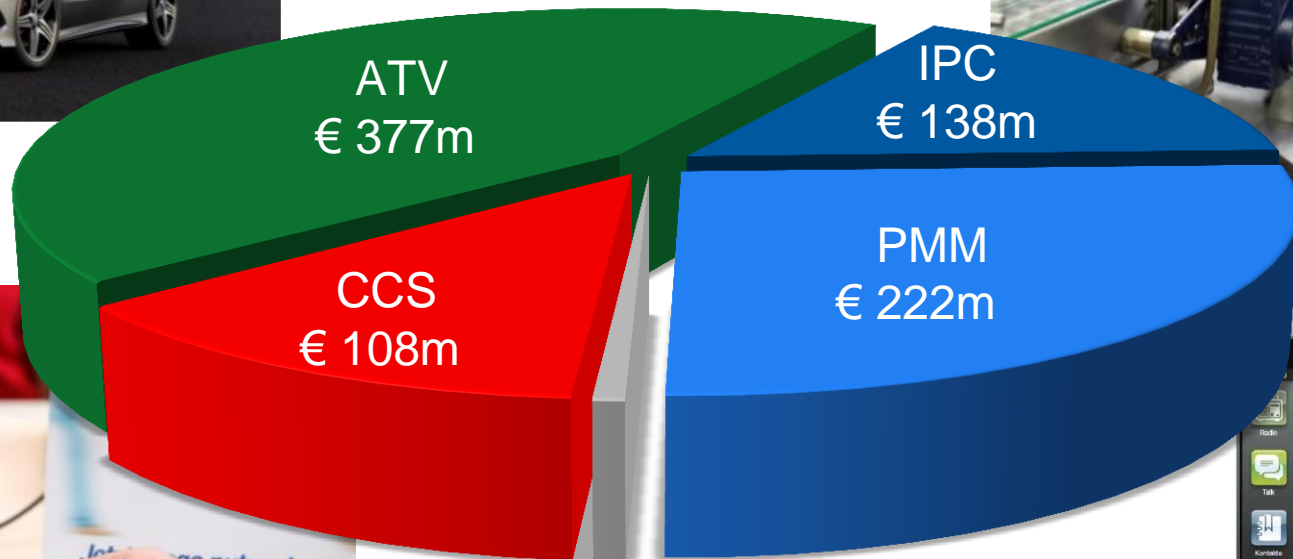
This presentation contains forward-looking statements about the business, financial condition and earnings performance of the Infineon Group.

These statements are based on assumptions and projections resting upon currently available information and present estimates. They are subject to a multitude of uncertainties and risks. Actual business development may therefore differ materially from what has been expected.

Beyond disclosure requirements stipulated by law, Infineon does not undertake any obligation to update forward-looking statements.

Revenue Split by Division

Q1 FY 2013 revenue: EUR 851m



OOS+C&E*
€ 6m

* Other Operating Segments;
Corporate and Eliminations.

Tight Customer Relationships are Based on System Know-how and App Understanding



ATV



IPC



PMM



CCS



Distributors

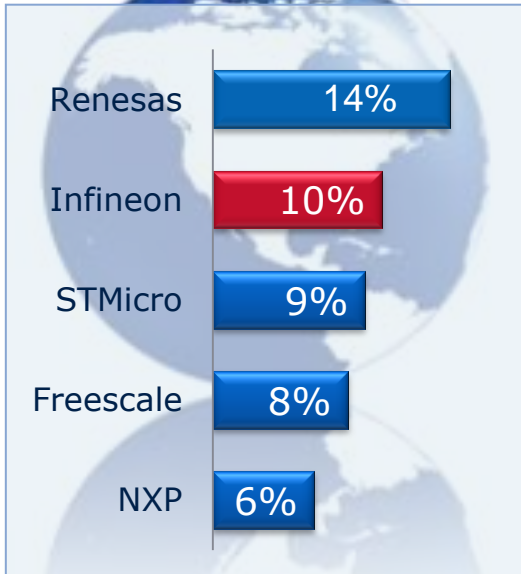


Infineon Holds Top Positions in All Target Markets



Automotive

#2

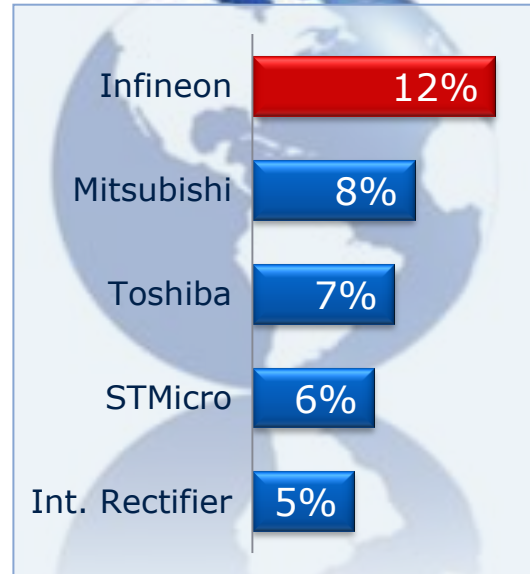


Calendar year 2011.

Source: Strategy Analytics, April 2012.

Power

#1

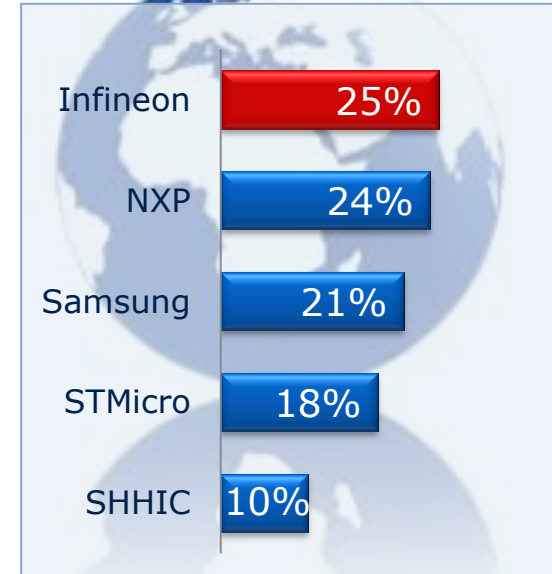


Calendar year 2011.

Source: IMS Research (an IHS company), July 2012.

Chip Card

#1



Calendar year 2011.

Source: IMS Research (an IHS company), August 2012.

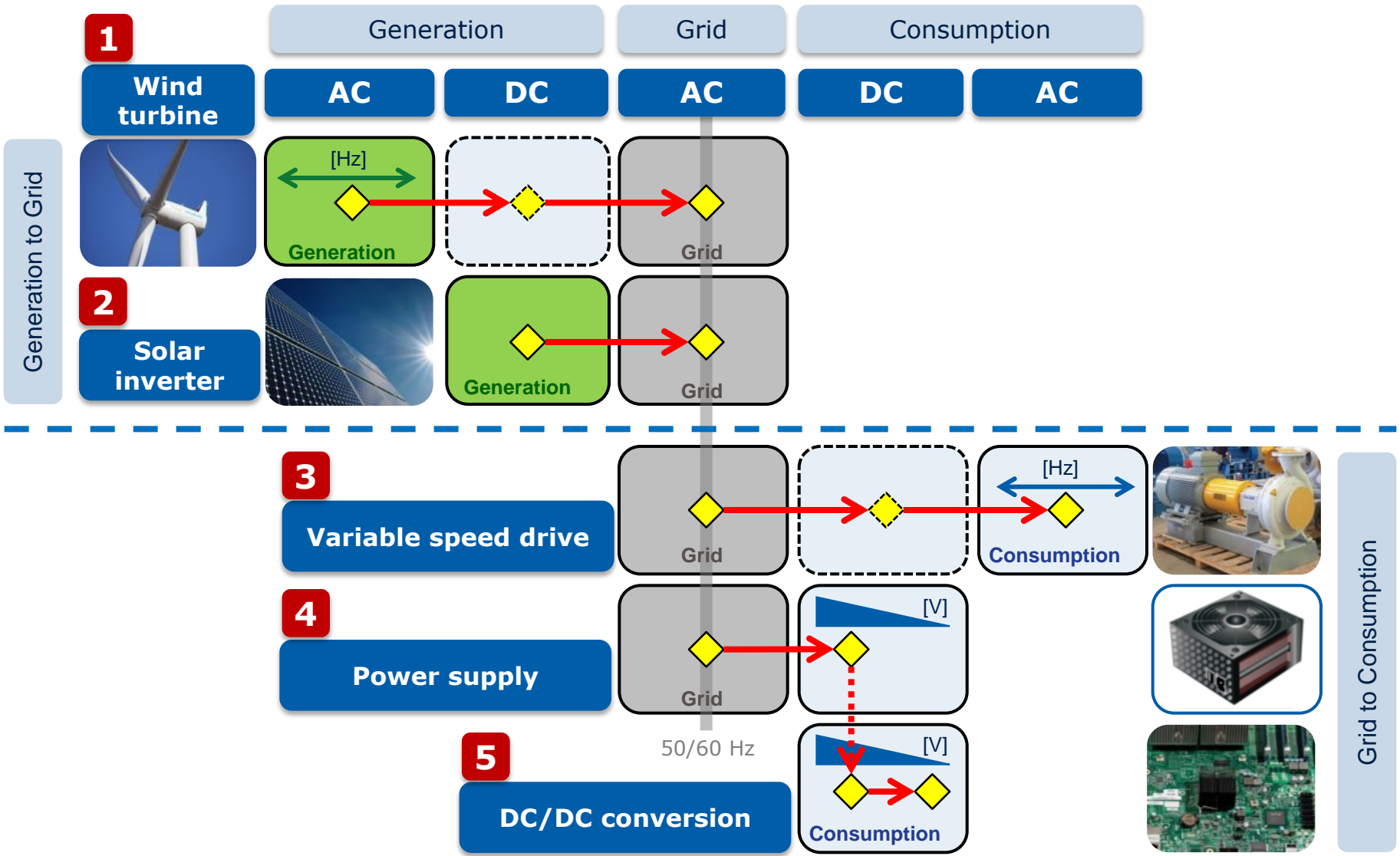
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Every Electricity Conversion Step Requires Infineon Components



Power Semiconductors Must Meet Challenging Application Requirements

Performance



- Efficiency
- Switching frequency
- Power density
- Thermal dissipation
- EMI behavior

Environment



- Wide operating temperature range
- Humidity
- Vibration
- Exhaust gases
- Cosmic ray hardness

Robustness



- Overvoltage
- Overtemperature
- Power cycling
- Overcurrent
- Short circuit
- Diagnostics

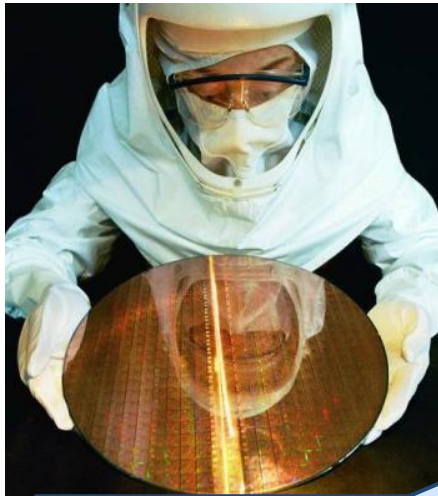
Quality



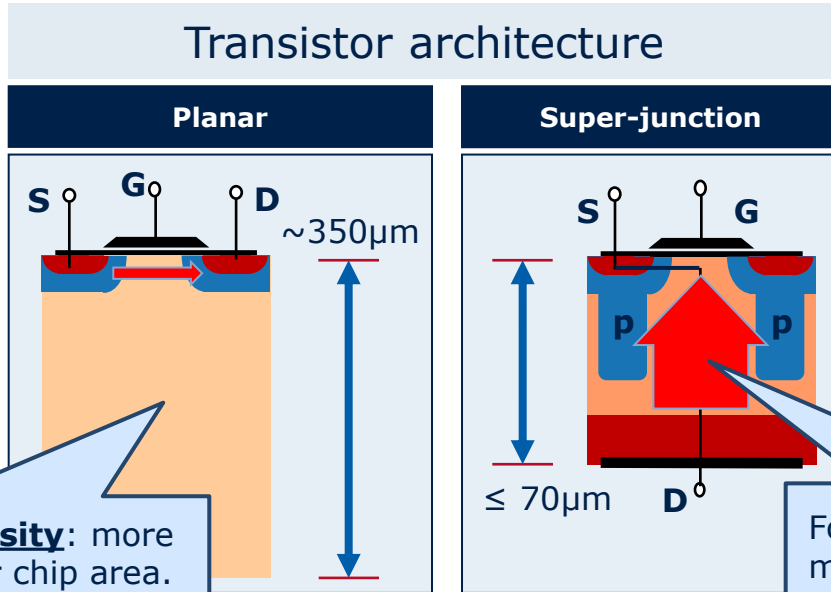
- Long end-product lifetimes
- Traceability (in the event of a recall)

CMOS versus Power Differences in Focus and Concept

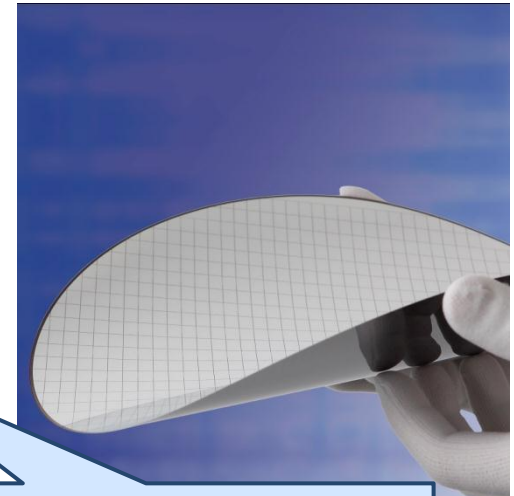
CMOS



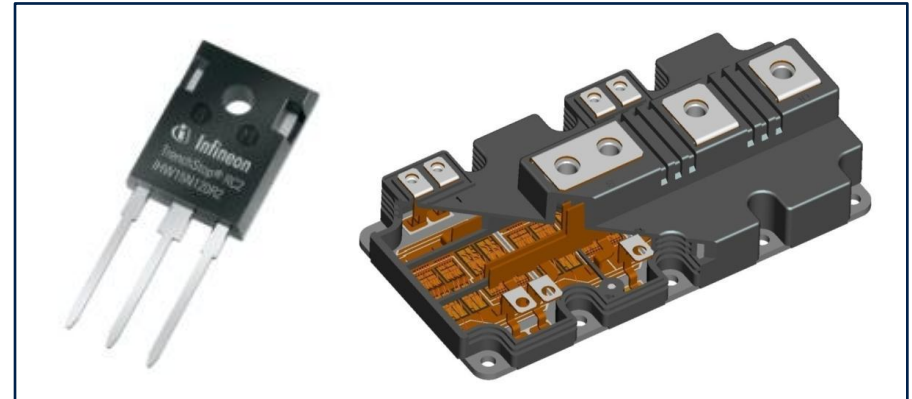
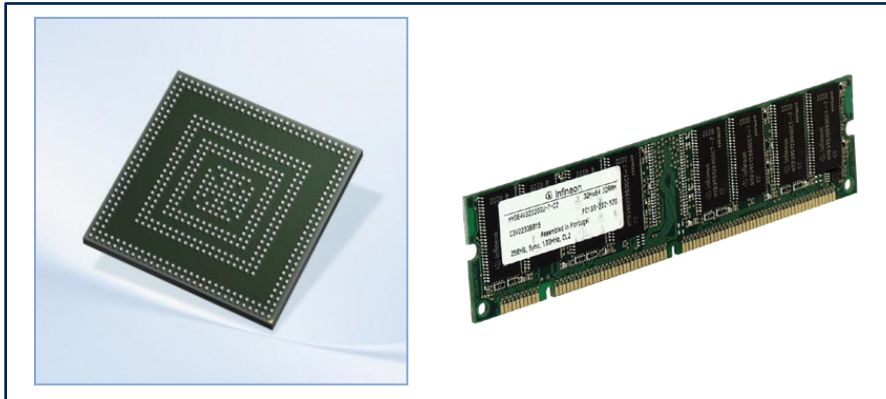
Focus on **transistor density**: more and faster transistors per chip area.



Power

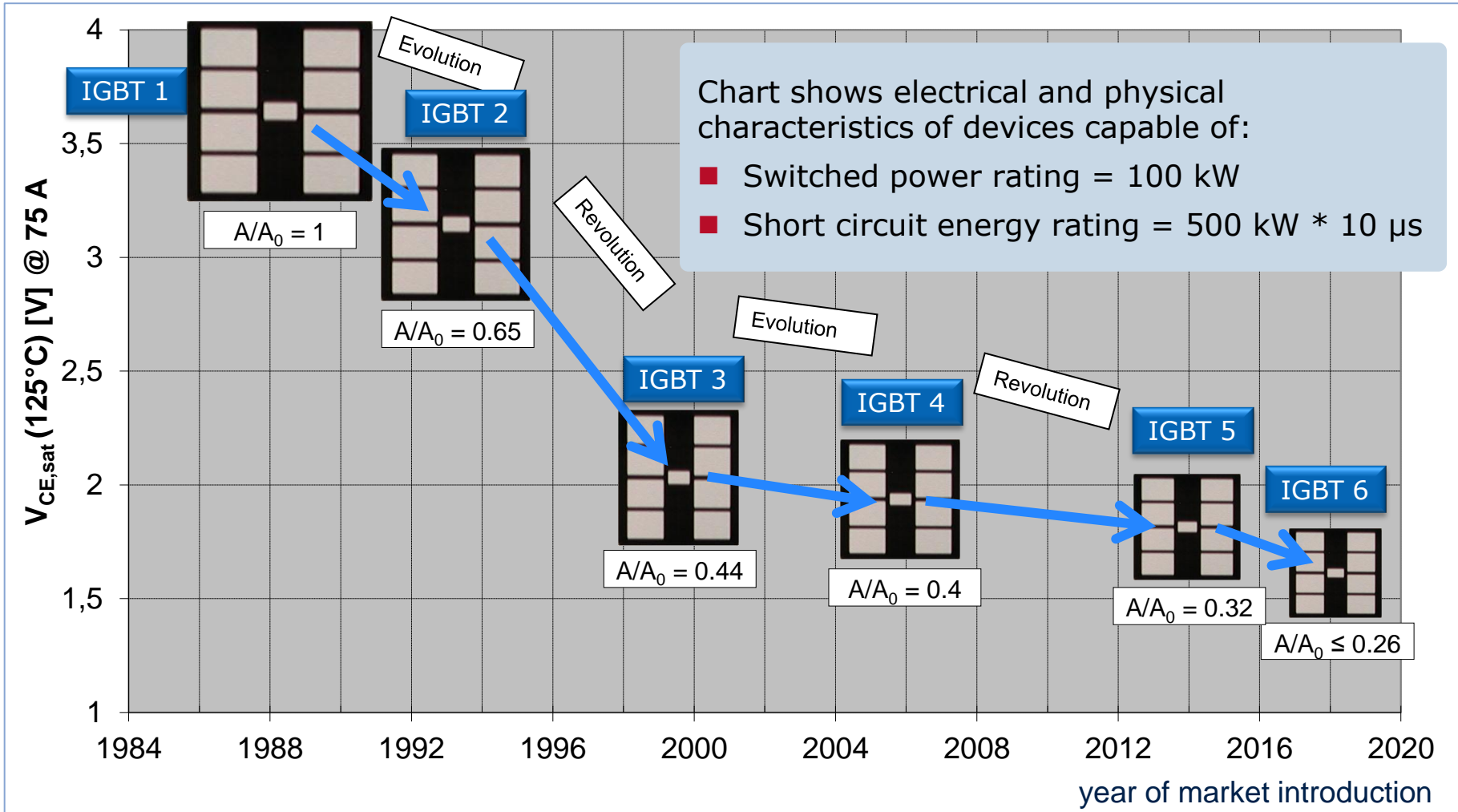


Focus on **power density**: more current per chip area.



Gains in Power Density Require Improved Device Characteristics All the Time

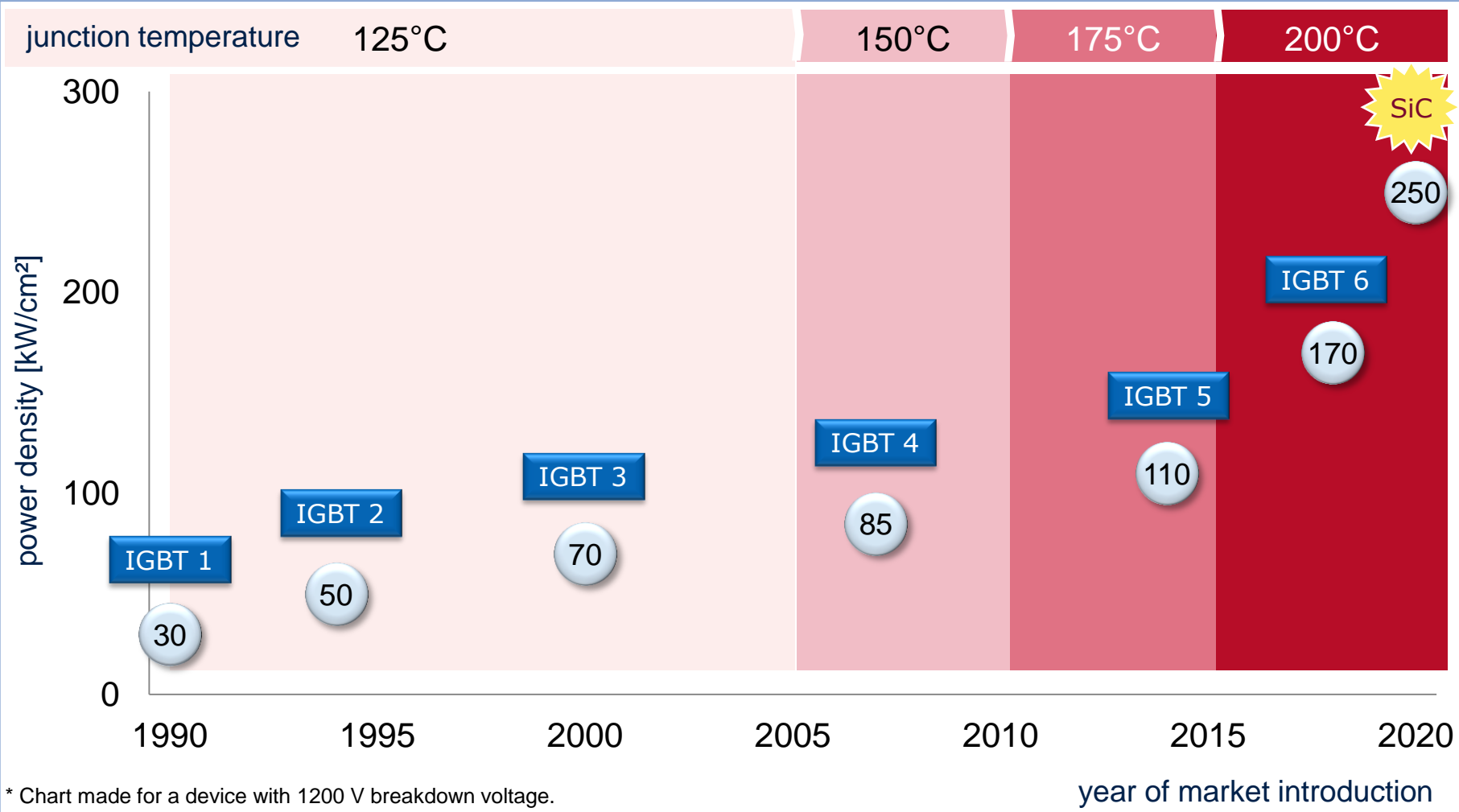
$V_{CE,sat}$ of 1200 V/75 A IGBT generations



Increase in Power Density Goes Hand-in-Hand with Higher Junction Temperatures



Development of power density of IGBTs*

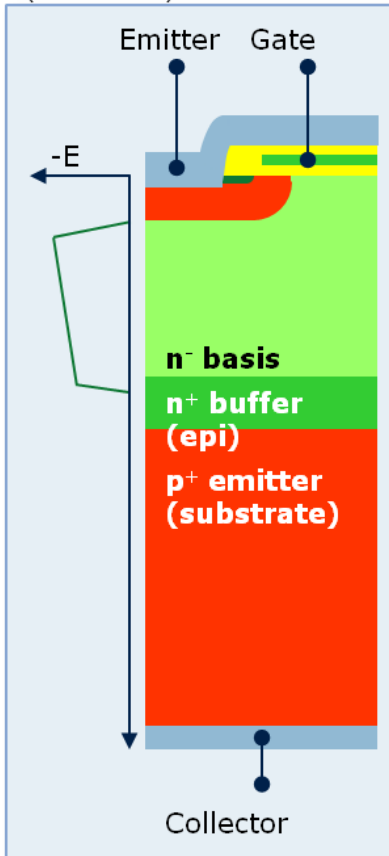


* Chart made for a device with 1200 V breakdown voltage.

Infineon Is The IGBT Innovation Leader

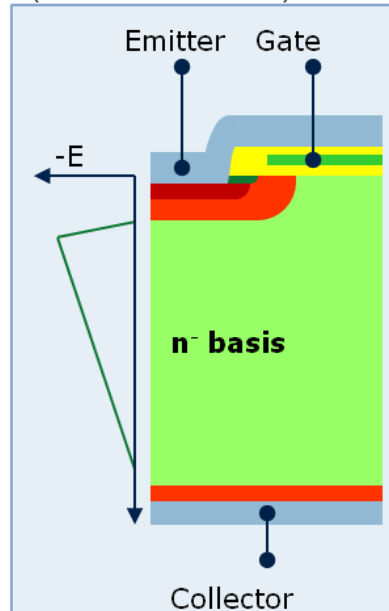
Punch Through

(ROW: 1988)



Non-Punch Through

(IFX: 1990 ROW:1997)

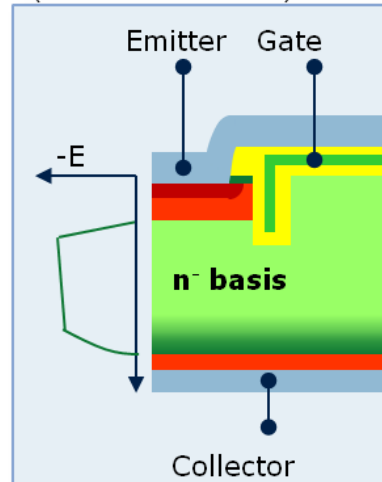


Advantages
 Implanted Back-Emitter
 better adjustable

Performance
 Lower Switching losses
 Higher Switching
 Robustness

IGBT 3 IGBT 4

(IFX: 2000 ROW:2006)

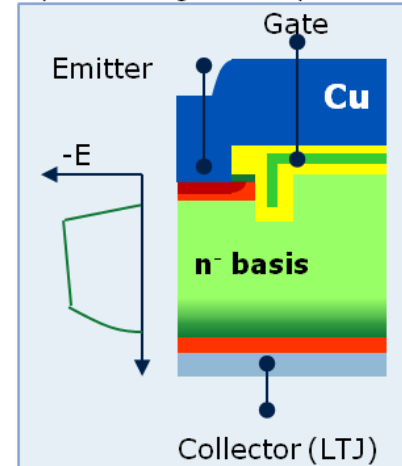


Advantages
 Implanted Back-Emitter
 Thinner Base Region

Performance
 Lower VCEsat
 Lower Switching losses
 Robustness like NPT

IGBT 5

(IFX: future generation)

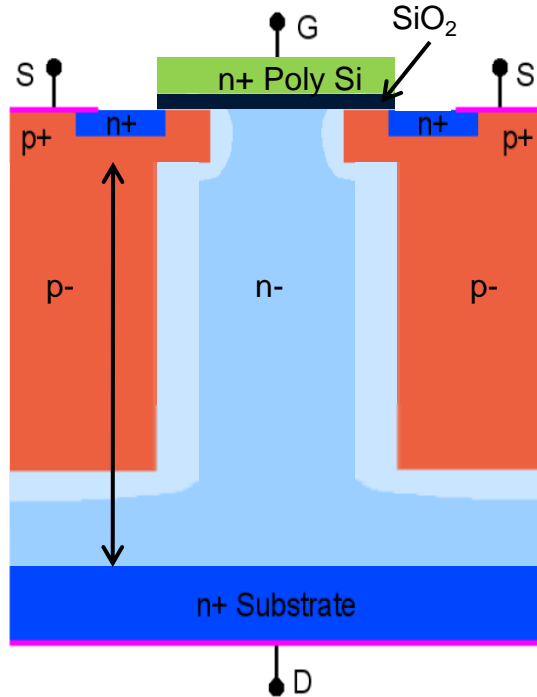


Advantages
 Even Thinner Base Region
 New Cell Design
 Copper Metallization
 LTJ

Performance
 Increased Lifetime
 Higher Current

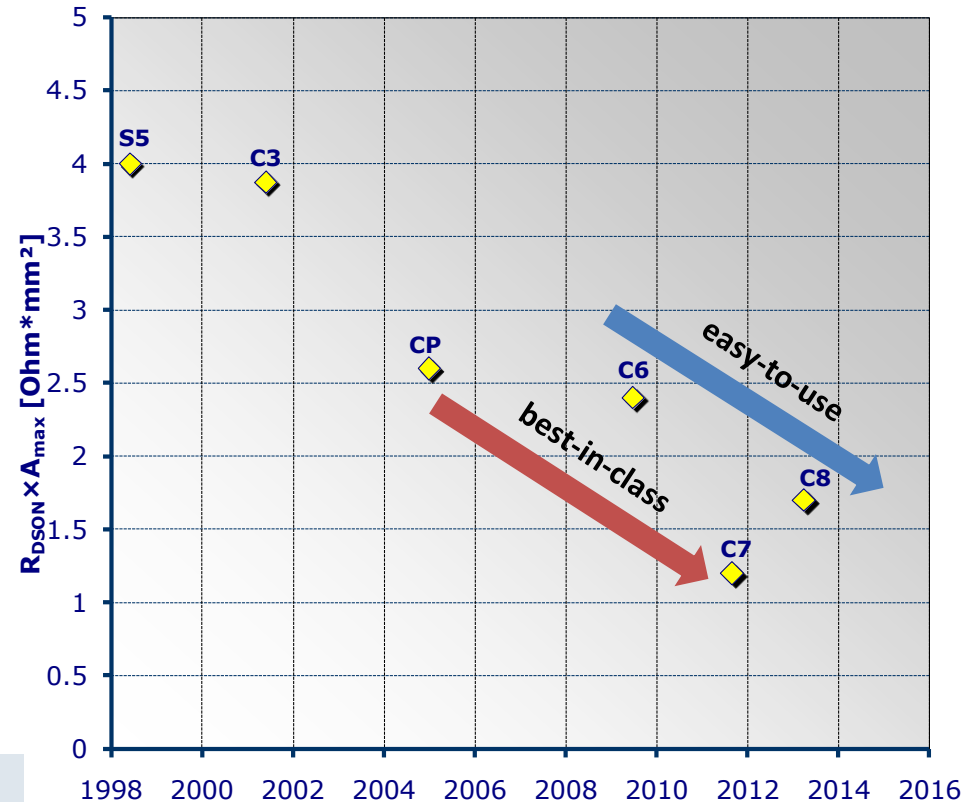
CoolMOS™ Is Break-Through High-Voltage MOSFET

Super-junction principle



- Principle of charge compensation allows drastic reduction of losses compared to traditional device concepts.
- Core know-how: structured epitaxy and thin wafer technology.
- Usable today from 500V to 900V.

CoolMOS™ generations



Adaption of product lines and targeted development to fit a broad variety of customer requirements

Infineon Further Advances Performance Through Innovations in Modules



Ever-increasing power density

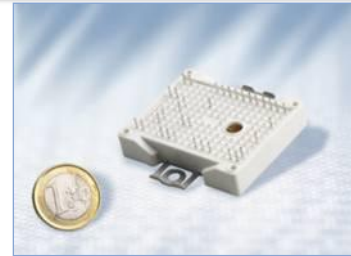
1995



2001



2007



2014



IGBT 2

- 34 mm module
- 1200 V
- T_j=125° C
- **0.46 kW/cm²**

IGBT 3

- EconoPACK™2
- 1200 V
- T_j=125° C
- **0.89 kW/cm²**

IGBT 4

- EasyPACK2B
- 1200 V
- T_j=150° C
- **1.16 kW/cm²**

IGBT 5 .XT technology

- PrimePACK™2
- 1200 V
- T_j=175° C
- **1.31 kW/cm²**

+280%*

T_j junction temperature.

* At constant junction temperature (125°C) it would have been > 180%.

Worldwide Front-End Production Sites



Regensburg

Employees: 1,283 (incl. temps)
 Capacity: 48k WSPM*
 Technology: > 200 nm
 Mainly ATV and PMM.

All figures as of September 2012.
 * Wafer starts per month (200 mm equivalent).



Villach

Employees: 2,000 (incl. temps)
 Capacity: 120k WSPM*
 Technology: > 200 nm
 Mainly ATV, IPC and PMM.

Dresden

Employees: 1,935 (incl. temps)
 Capacity: 45k WSPM*
 Technology: ≥ 90 nm
 Mainly ATV and CCS.



Kulim

Employees: 1,333 (incl. temps)
 Capacity: 85k WSPM*
 Technology: > 200 nm
 Mainly ATV, IPC and PMM.



Worldwide Back-End Production Sites

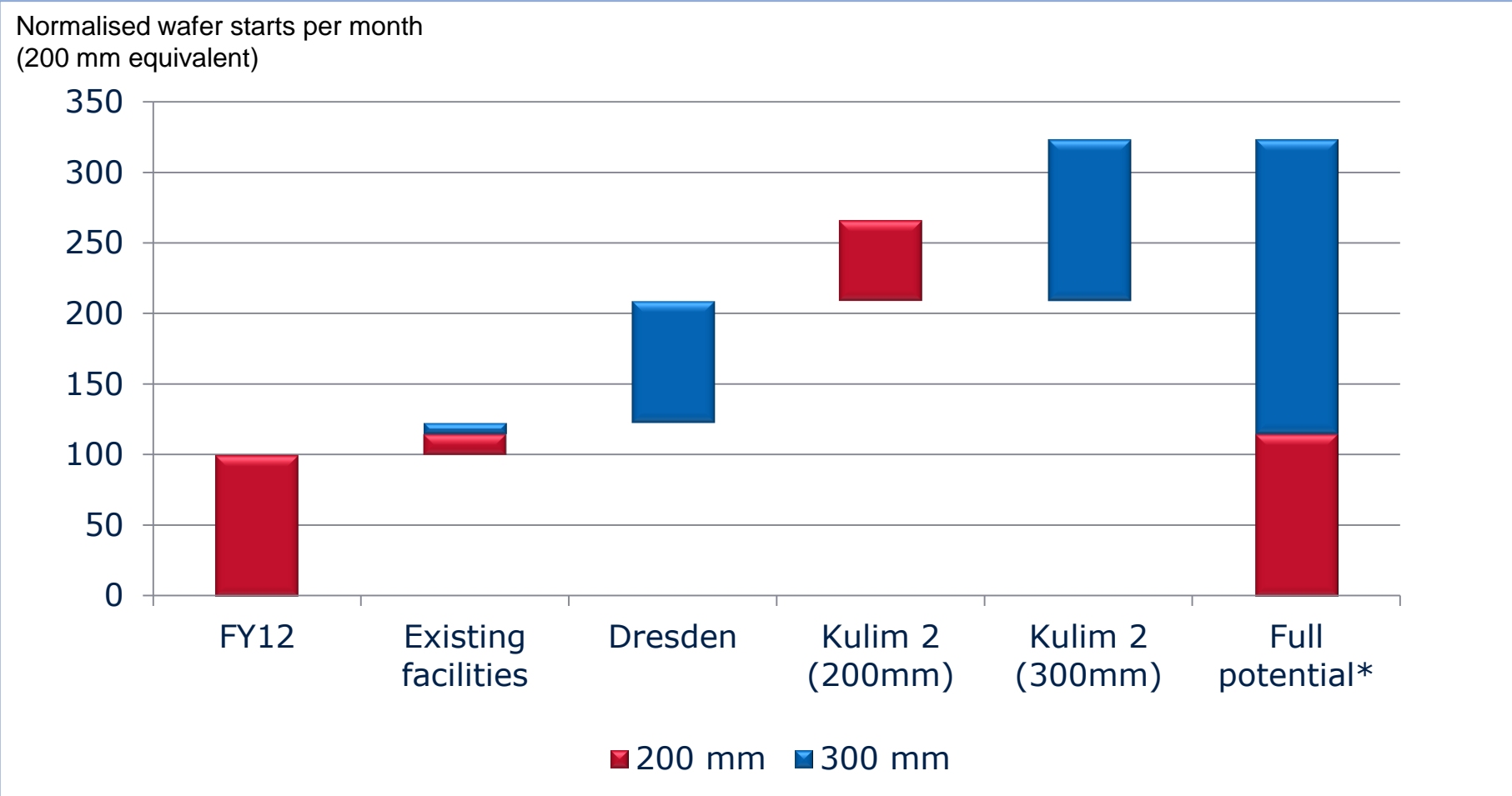


All figures as of September 2012.

Existing Fab Landscape Sufficient to Roughly Triple Present Capacity

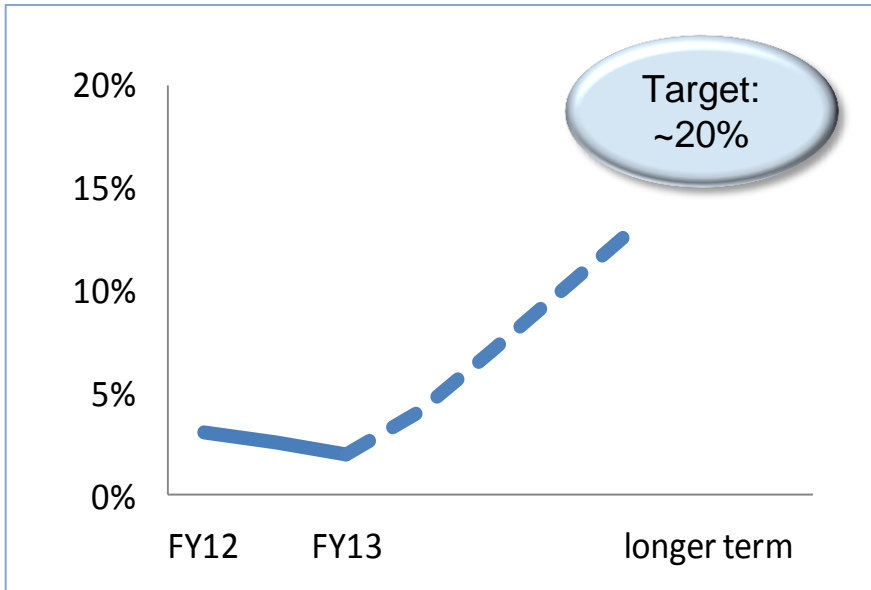


Front-End capacity bridge for power semiconductors



* Assuming Kulim 2 (300 mm).

Front-End outsourcing share



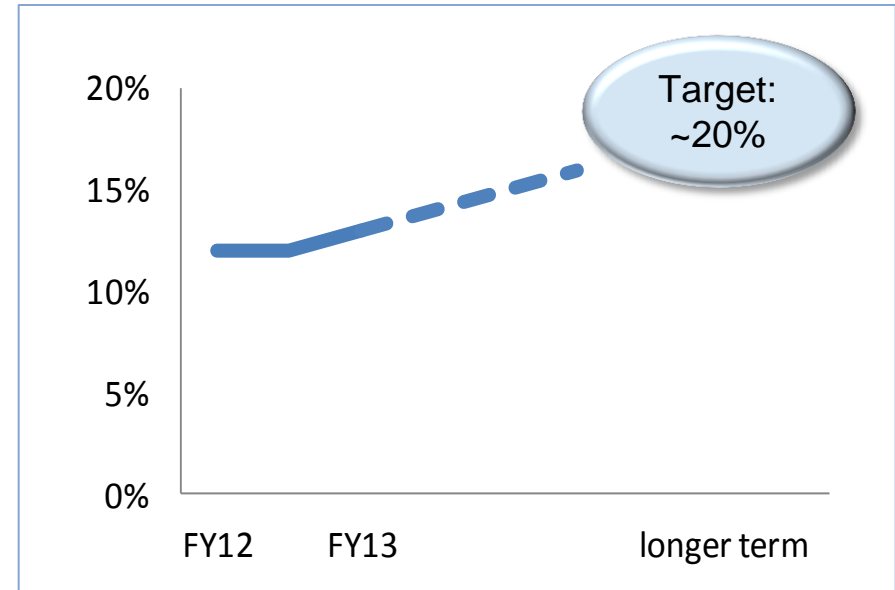
Status:

- For nodes ≥ 90 nm foundries are mainly used to optimize capex and for flexibility.

Outlook:

- Share at foundries will increase in the next five years driven by the ramp of eFlash technologies in 90 nm, 65 nm and 40 nm.

Back-End outsourcing share



Status:

- Outsourcing primarily on low-power devices and complex CMOS.

Outlook:

- Assembly and test outsourcing will continue to expand with Infineon's growth in the areas of power and CMOS.

Investments in FY 2013 to Be Reduced Significantly



Investments

D&A

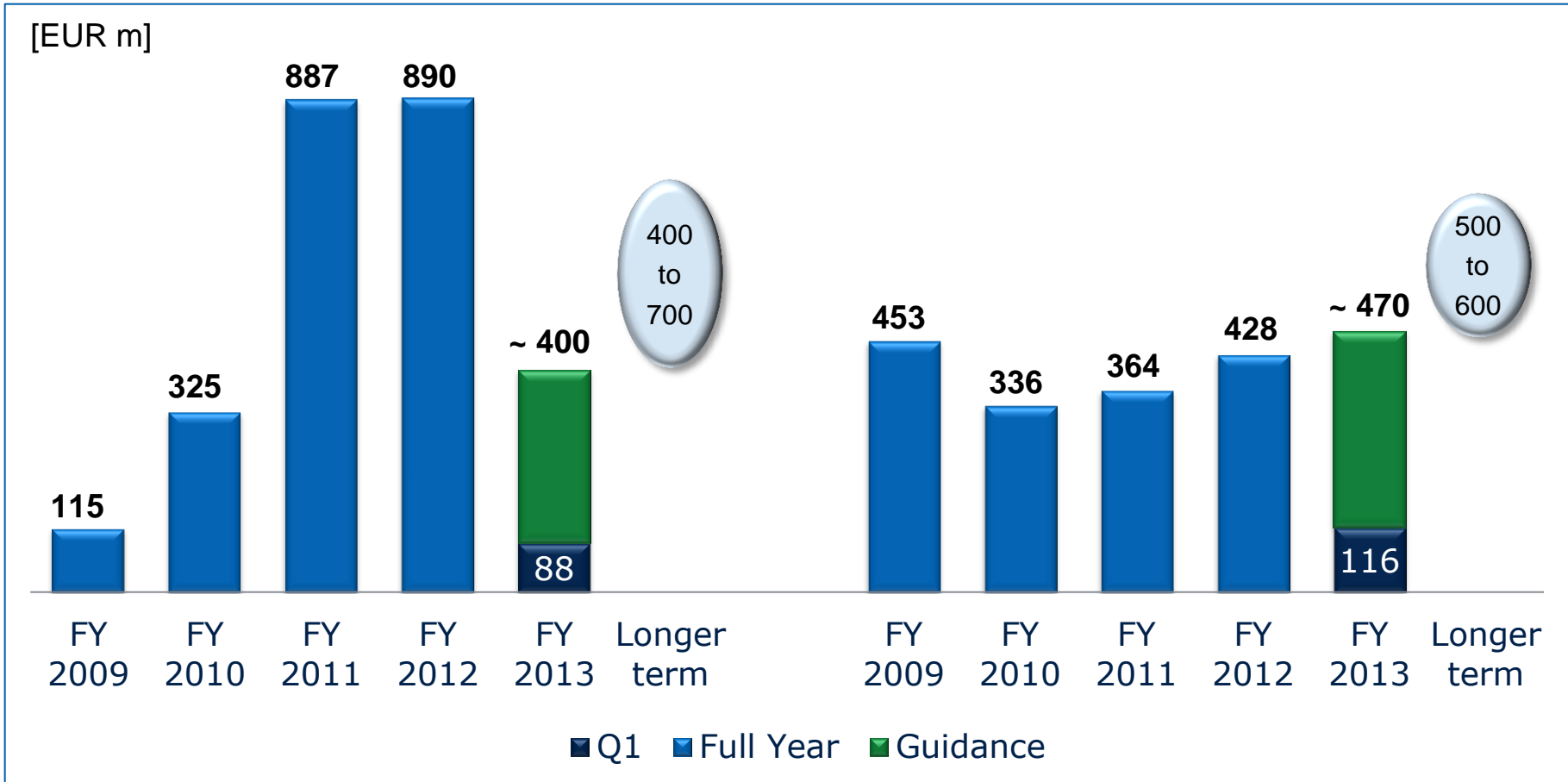


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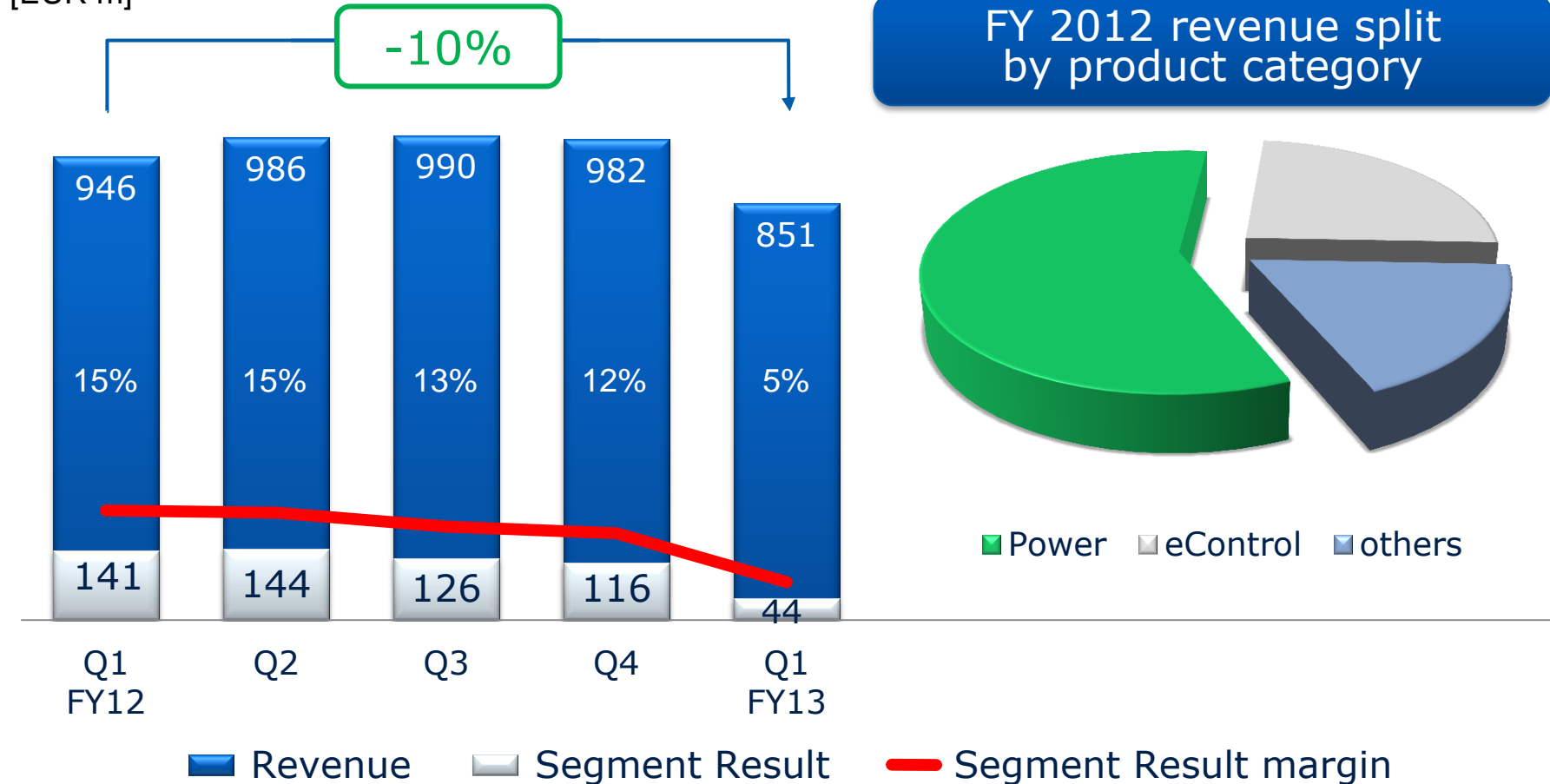
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Lower Revenue And Segment Result Due To Market Weakness

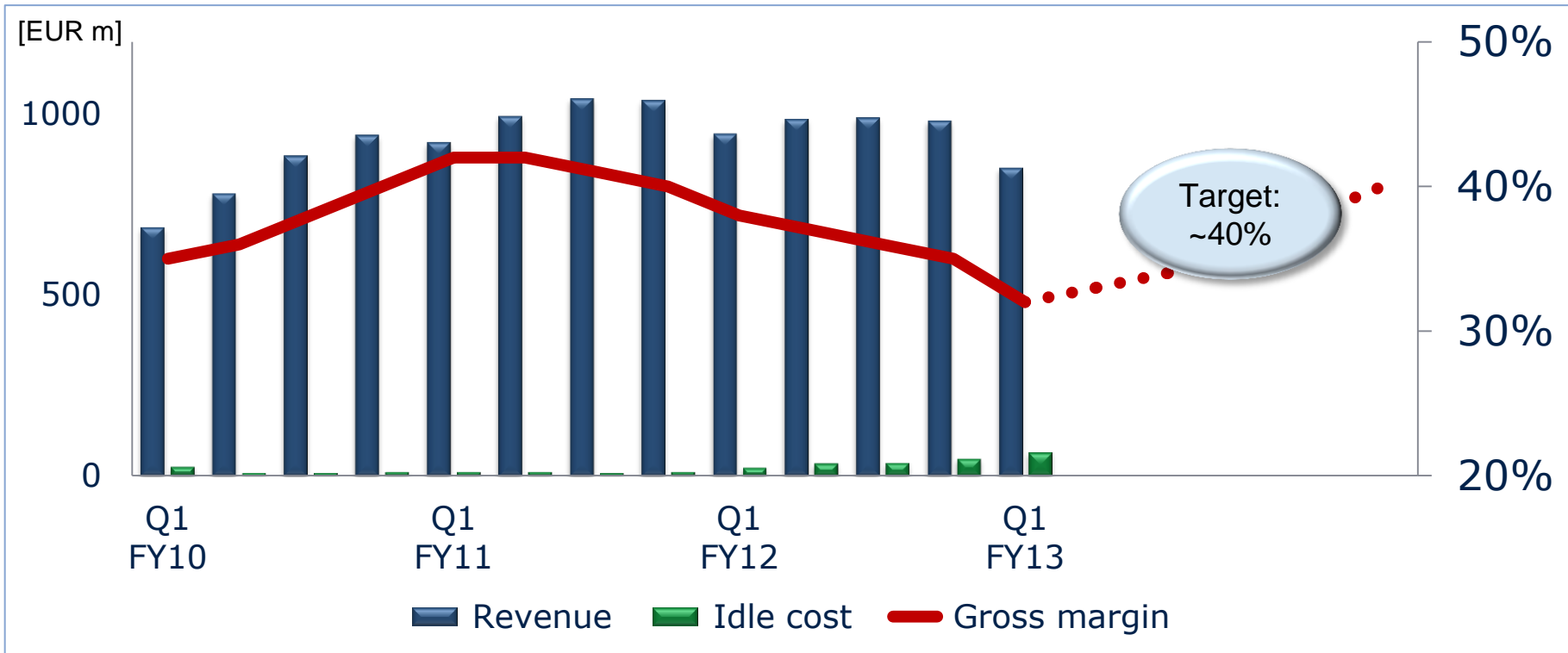
Revenue and Segment Result

[EUR m]



Gross Margins Will Improve As Utilization Increases

Revenue and gross margins

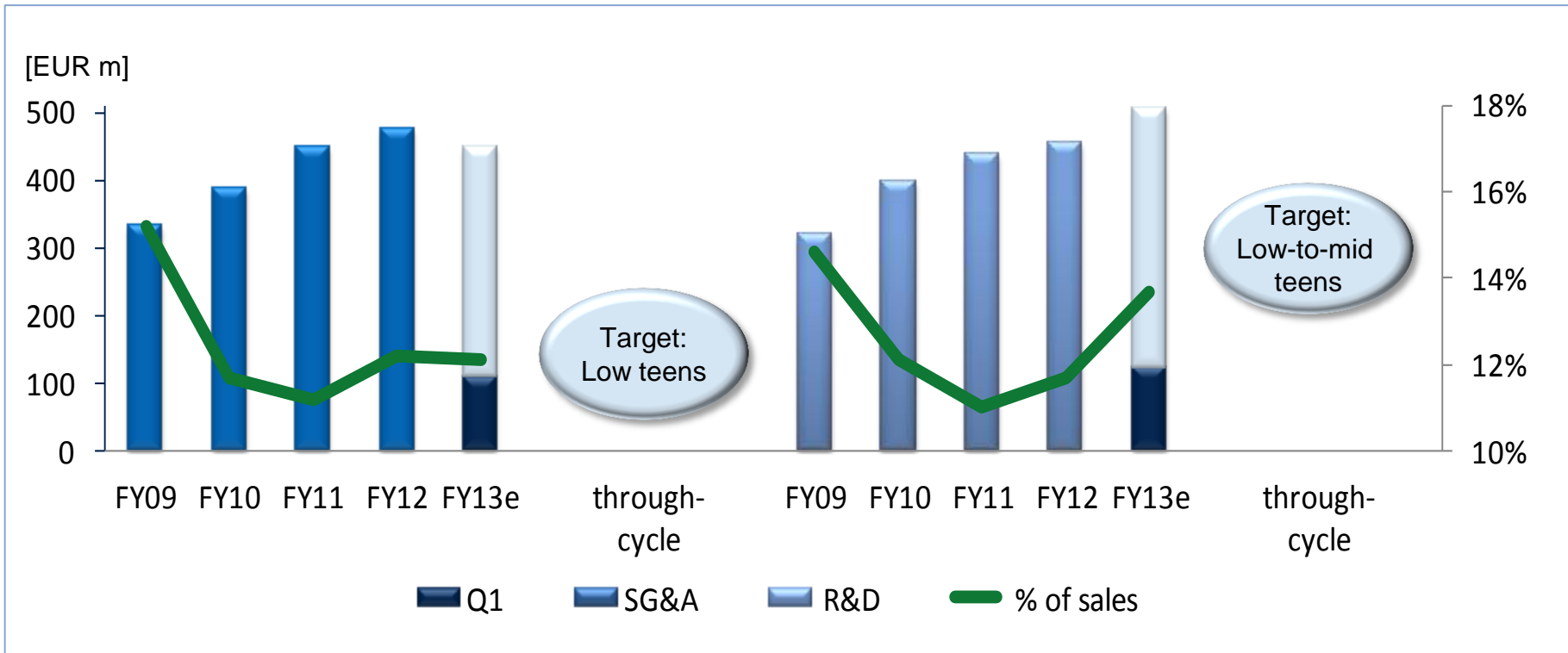


- Capacity in place today for annualized run rate of EUR ~4.5bn.
- Utilization rate in Q1 FY13 at ~75%.
- Incremental gross profit increase of €0.75 for each €1.00 revenue increase.

OpEx In-line With Target Operating Model

S and G&A

R&D

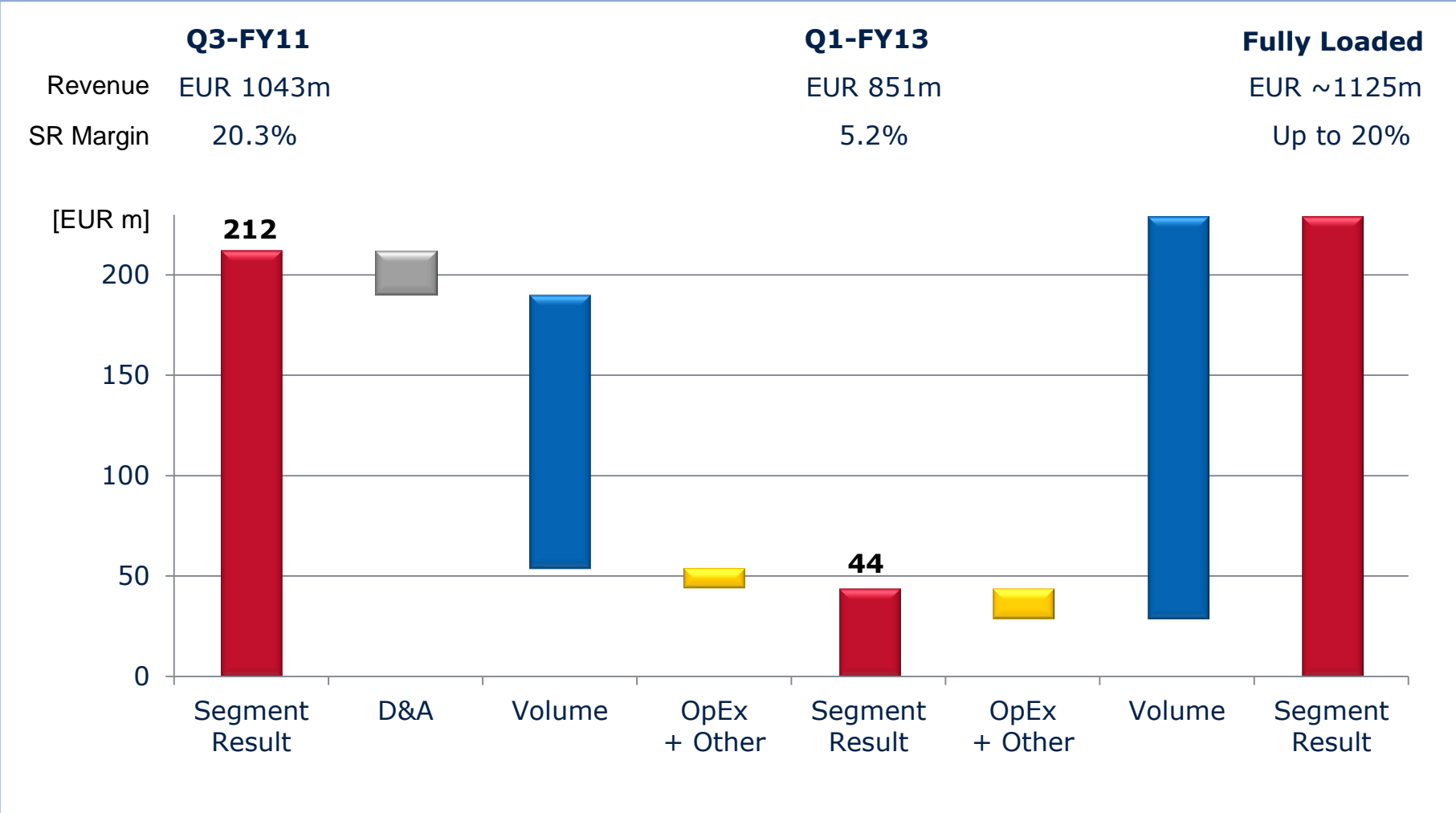


- R&D and SG&A resources are in place to fulfill EUR ~4.5bn annual run rate.
- OpEx growth to be limited to inflation and performance-based payments in FY13 and FY14.

Profitability Determined Mainly by Utilization Levels



Segment Result bridge: Q3-FY11 vs. Q1-FY13 vs. Fully Loaded



Guidance for Q2 FY13 and FY 2013

Outlook Q2 FY13*
(compared to Q1 FY13)



Outlook FY 2013*
(compared to FY 2012)



Revenue

Mid single digit percentage increase.

Mid-to-high single digit percentage decline.

Segment Result / Margin

Segment Result slightly up in absolute terms.

Segment Result Margin in the mid-to-high single digits.

Investments in FY 2013

About EUR 400m.

D&A in FY 2013

About EUR 470m.

* This outlook is based on an assumed Euro/US Dollar exchange rate of 1.30.



ENERGY EFFICIENCY MOBILITY SECURITY

Innovative semiconductor solutions for energy efficiency, mobility and security.

